Call for Papers

During the past decade, ReCoSoC has established itself as a reference international event for researchers in the areas of reconfigurable and communication-centric systems-on-chip. Its informal and dynamic philosophy encourages the technical and scientific interactions between senior academics and young researchers.

All accepted papers will be published in IEEE Xplore. The authors of the best papers will be invited to submit an extended version of their contribution to the journal Microprocessors and Microsystems: Embedded Hardware Design (MICPRO, Elsevier).

The areas of interest include (but are not limited to):

• New paradigms for reconfigurable and communication-centric computing
• Reconfigurable and adaptive embedded SoCs
• Communication-centric design techniques at different abstraction levels
• On-chip communication architectures
• Low power design of reconfigurable and multiprocessor SoCs
• Communication-aware multiprocessor embedded systems
• OS and middleware for reconfigurable and multicores SoCs
• Specification languages and design methodologies
• Verification and evaluation techniques
• Industrial case studies

Submission:

Authors are invited to submit contributions as maximum 8 page papers in IEEE conference format. Contribution(s) have to be submitted electronically through the EasyChair portal of the conference:

https://easychair.org/conferences/?conf=recosoc2016

Important Dates:

Abstract submission deadline: 1st April 2016
Full paper submission deadline: 15th April 2016
Notification of acceptance: 6th April 2016
Camera-ready due: 15th May 2016
11th International Symposium on Reconfigurable Communication-centric Systems-on-Chip

June 27–29, 2016, Tallinn, Estonia

Call for Papers

Special Sessions

The ReCoSoC technical program will incorporate Special Sessions, with the objective of complementing the regular program with new and emerging topics in the field of reconfigurable and communication-centric systems-on-chip. In 2016, ReCoSoC will have four Special Sessions:

Secure multi/many-core architectures in the era of Internet-of-Things
Organized by: Martha Johanna Sepulveda (TUM, Germany)
Vianney Lapotre (UBS, France)
Guy Gogniat (UBS, France)

Energy Technologies for Reconfigurable Systems in the Manycore Era
Organized by: Davide Patti (University of Catania, Italy)
Maurizio Palesi (University of Enna – KORE, Italy)

System Design for Heterogeneous 3D-Architectures
Organized by: Thilo Pionteck (Universität zu Lübeck, Germany)
Alberto Garcia-Ortiz (Universität Bremen, Germany)
Gilles Sassatelli (LIRMM, France)

Smart Health
Organized by: Hassan Ghasemzadeh (Washington State University, US)
Masoumeh Ebrahimi (UTU, Finland and KTH, Sweden)

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More information
www.recosoc.org